### fidus innovate•design•deliver

# FPGA Design

### UTILIZE OUR EXTENSIVE EXPERIENCE IN FPGA DESIGN FROM LOW-COST CPLDS RIGHT UP TO THE LARGEST FPGAS IN THE WORLD.

Your teams are facing unique challenges, and we understand too many projects and too few engineering resources. And that's not all. Designing for your complex prototype or product requires deep technical knowledge as well as thoughtful consideration and planning. Bottom line, you need to meet your roadmap schedule, and with Fidus as an extension of your team, you will get there, and get it right the first time.

#### **HOW WE HELP**

# Delivering solutions at the speed of your business.

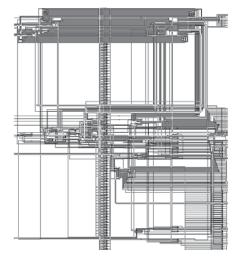
After more than 20 years, 3,000 projects and 400 customers, we've learned how to transform your idea, vision or concept into the product you've imagined. That's because we know how to take complex problems, and design and deliver them against dynamic environments and tight deadlines.

Your project needs the right team and the right toolset with a rigorous process to deliver on your capability and capacity challenges. At Fidus, we become a seamless extension of your team, with a clear focus and commitment to getting your design or prototype to market faster. Being a Xilinx<sup>®</sup> Premier Design Services member means that we are trained and adept at selecting and implementing the most advanced Xilinx devices and tool flows. And since Fidus attracts a variety of industries and customers, our teams have cultivated creative problem solving, ensuring projects are efficiently planned and cost effective.

**That's not all!** Our FPGA design teams' skills are well complemented by our hardware, PCB layout, signal integrity, embedded software, and mechanical design expertise.

## Extend your team — you choose how we work together.

Design projects have many elements, all of which are in your control when you work with Fidus. Do you want to hand off the whole project or just find a few people to supplement your



staff? Looking for project management to keep you informed and the project on track? Our breadth of expertise, creative team structures, and flexible payment options allow a customized service that works for you:

- **Turnkey design services:** Plan and execute with our team for an end-toend development solution, or choose anything in between to suit your requirements.
- Staffing services: Utilize a design expert at your virtual or physical location, for the length of your project, or for a targeted engagement.

#### **DESIGN EXPERTISE**

**Turnkey:** FPGA design, verification, and documentation solutions

**Device selection:** Identifying the best device to get the job done

**Device retarget:** Helping you migrate from one FPGA to another

Languages: Verilog<sup>®</sup>, VHDL, SystemVerilog, HLS

Xilinx advanced tool flows: Vitis<sup>®</sup>, SDNet<sup>™</sup>, Partial Reconfiguration, HLS, IDF, AMP, SDSoC<sup>™</sup>, MatLab<sup>®</sup>, Simulink<sup>®</sup>, System Generator for DSP<sup>™</sup>

Intel<sup>®</sup> advanced tool flows: SoC Embedded Development Suite

**ASIC-to-FPGA Conversion:** Replacing low-volume or discontinued ASICs with low cost FPGAs

**ASIC prototyping in FPGAs:** De-risking ASIC developments by first implementing the design in one or multiple FPGAs

High performance computing: Tensor Processing Unit, ML, AR/VR

Multi-Gigabit Serial Links: PCIe® Gen4, JESD204, Aurora

Memory Interfaces: NVMe, HBM, DDR4, SRAM, LPDDR, etc.

**Communication Protocols:** OTN, CPRI<sup>™</sup>, TCP/IP, Ethernet, SONET/SDH, ATM.

#### **Digital Signal Processing (DSP):**

Software Defined Radio (SDR), filters, echo-cancellation, 802.11 a/b/g wireless LAN, etc. Video: 12G-SDI, DP, MIPI, HDMI, HDCP, image enhancement, scaling, overlay, PiP, soft-core processor engine with DMA interfaces, etc.

Experience with: Xilinx (UltraScale+<sup>™</sup>, UltraScale<sup>™</sup>, Kintex<sup>®</sup>, Virtex<sup>®</sup>, Zynq<sup>®</sup>, Artix<sup>®</sup>, Spartan<sup>®</sup>), Intel<sup>®</sup> (Stratix<sup>®</sup>, Arria<sup>®</sup>, Cyclone<sup>®</sup>), Lattice

#### TOOLS FOR HIGH-END DEVELOPMENT

Xilinx: Vitis, Vivado®, ISE®, PetaLinux

Intel: Quartus Prime

**Embedded:** Xilinx (MPSoC, ARM<sup>®</sup>, PowerPC<sup>®</sup>, MicroBlaze<sup>™</sup>, Linux on MicroBlaze, PicoBlaze<sup>™</sup>, Zynq, bare metal, EDK/SDK), Intel (SoC, NIOS)

Simulation/Code Coverage: Questa®, ModelSim® SE, NC-Sim

**Synthesis:** Synplify Pro<sup>®</sup>, Synopsys Design Compiler<sup>®</sup>

Lab tools: Programming pods, Vivado<sup>®</sup> Logic Analyzer, ChipScope<sup>™</sup>

#### **EXAMPLES OF OUR WORK**

- Video aggregation of 10 video streams into a single custom fiber link.
   Technologies: Xilinx Kintex UltraScale, Xilinx Vivado, GTY, DP, 12G-SDI, DVI
- HDCP IP Core Development Designed, tested, and integrated, Xilinx-targeted HDCP IP. Technologies: HDCP 1.3 for DisplayPort: HDCP encryption/decryption for SST, HDCP 1.4 for HDMI: HDCP encryption/decryption for HDMI 1.4b

- Video MIPI DSI input to MIPI DSI with low latency processing unit for VR application.
   Technologies: Xilinx Virtex, ASIC emulation, HLS
- Video protocol conversation from and to SDI, DP, HDMI, MIPI. Technologies: Xilinx, HLS
- High Bandwidth NVMe Storage Systems.
   Technologies: Xilinx Zynq UltraScale+, NVMe, SSD, PCle
- 100G Ethernet Switch/Protocol analyzer and tester.
   Technologies: Xilinx Virtex UltraScale+
- Our FPGA, SI, and layout expertise with Xilinx high-speed transceivers makes us a one-stop shop for highspeed serial. Technologies: Xilinx GTX/GTH/GTZ/GTY
- EPON ONU development. Technologies: Xilinx Virtex UltraScale+, SDNet
- Semiconductor Test System
  Technologies: Intel Cyclone V SoC, RTOS
- VME SDR airborne search and rescue radar system. The center-piece of the system is a custom FPGA-based software defined radio DSP engine. The DSP algorithms were designed in MATLAB and then moved into VHDL. Technologies: AIS, SDR (software defined radio), Xilinx FPGA, PowerPC<sup>®</sup> hardcore, VHF, AGC, programmable attenuators, power amplifier, VME, VITA, DO-160E, ITU M1371, NMEA0813
- Encryption algorithms on Xilinx Zynq using Asymmetric Multi Processing, HLS, IDF, and Partial Reconfiguration. Technologies: Xilinx Zynq, Avnet<sup>®</sup> Zedboard, AMP, HLS, IDF, PR, AES, SHA2, SHA3

### **XILINX.** ALLIANCE PROGRAM PREMIER MEMBER

#### **BRINGING YOU XILINX PREMIER**

As Xilinx Premier, Fidus receives exclusive training, certification, and early-access to tools, IP, and new silicon. By invitation, Fidus was the inaugural Xilinx Premier Design Services member in North America. So what does this mean? It means that when you hire Fidus, you know that Fidus is on the forefront of Xilinx's roadmap, experienced in the most advanced tool flows, and is top of mind within the Xilinx support network.

#### DESIGN SOLUTIONS WITH FIDUS

#### Need prototype and product design help?

We'll work with you to understand what you're looking for, and we'll dedicate the necessary resources to make sure it's a success the first time. Come to us with just an idea or specific challenges that are keeping you up at night, and we'll help you solve them.

Fueled by 20+ years' experience, our expertise, and creativity, along with our collaborative and process driven approach, turns complex challenges into well-designed solutions, and we keep customers like you coming back, again and again:

- 1. We are committed to "first time right."
- 2. Experience has taught us how to solve problems on any scale.
- 3. Faster time to market means faster time to benefit.
- 4. You choose how we work together.
- 5. Unique projects are our obsession.
- 6. We believe transparency builds trust.
- 7. Customer focus is our calling card.

20+ years experience

Collaborating with smart teams is what fuels us every day.



Your unique challenges are our obsession.



Extending your team with our expertise brings designs to market faster.



Customers love to work with us, again and again.

#### **ABOUT FIDUS**

Fidus Systems, founded in 2001, specializes in leading-edge electronic product development with offices in Ottawa and Waterloo, Ontario, and San Jose, California. Our hardware, software, FPGA, verification, wireless, mechanical and signal integrity teams work to innovate, design and deliver next-generation products for customers in emerging technology markets. Fueled by 20+ years' experience and creativity, along with our collaborative and process driven approach, we turn complex challenges into well-designed solutions. And with over 400 customers and 3000+ completed projects, we have the expertise to be a seamless extension of your team, providing a clear focus and commitment to getting designs and prototypes to market faster. Once you start working with us, you'll trust us like one of your own. Our hallmark is transparency. Our guiding principle is first time right.



The Fidus name and the Fidus logo are trademarks of Fidus Systems Inc. Other registered and unregistered trademarks are the property of their respective owners. ©Copyright 2021 Fidus Systems Incorporated. All rights reserved. Information subject to change without notice. fidus.com