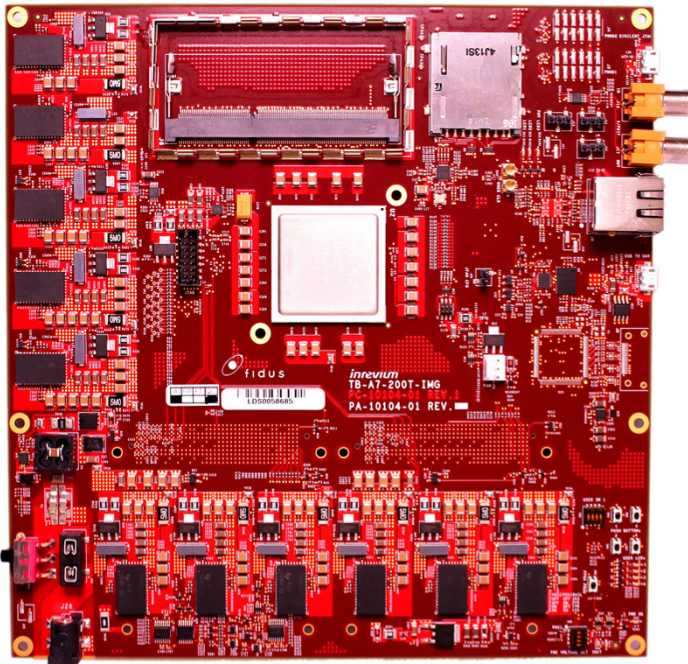


ACDC A7: Artix®-7 Development Platform

March, 2015

Now accepting orders

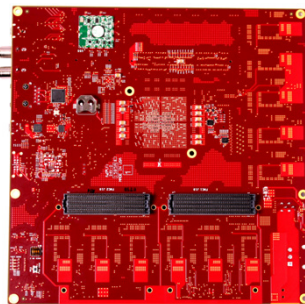


Features

- Xilinx® Artix®-7 XC7A200T-2FFG1156C FPGA
- 2 FMC HPC interfaces, with 8 MGT connections per FMC
- 1GB DDR3 SDRAM SODIMM
- Flexible clocking architecture
- Compatible with FMC Specification (VITA 57.1)
- Designed for electrical compatibility with most FMC cards.*
- Proven operation with inrevium DP1.2 and HDMI4K.
- Compact size for easy, stylish packaging
- Low cost development solution

* Verify your target FMC with us prior to ordering

**BOTTOM
VIEW**



Product Outline

Featuring

- Xilinx® Artix®-7 XC7A200T-2FFG1156C FPGA
- Two HPC FMCs connectors (located on bottom)

Memory

- 1GB DDR3 SDRAM SODIMM
- Maximum theoretical throughput: 51.2 Gbps

Interfaces

- 2 FMC HPC interfaces, featuring
 - 8 multi-gigabit transceiver connections per FMC
 - High-speed LVDS (FMC1: 58, FMC2: 46)
 - Independent VADJ setting to 1.5, 1.8, 2.5, or 3.3V
- UART over USB support
- JTAG over USB support or via onboard header
- 10/100/1000Base-T RJ45 (RGMII interface to FPGA)
- S/PDIF in/out support for digital audio
- Miscellaneous
 - 8 DIP Switches
 - 4 push-button tact switches
 - 4 green LEDs

Clocks

- SiLabs® SI5338B clock generator
- 200MHz system clock
- Additional configurable system clock
- 2 configurable FMC MGTRECLK's to FPGA
- 2 MMCX clock inputs

Configuration

- JTAG download or via onboard Quad SPI FLASH

Power

- External 12V DC Power Supply (included)
- Fan for FPGA cooling

Board Dimensions

- 200mm x 200mm (approx. 8" x 8")

Available References

Design Package (available under license)

- Schematics, PCB Layout, Artwork, Bill of Materials

FPGA Reference Designs

- Downloadable .bit file examples
- Licensable source (some blocks netlist encrypted)

For Inquiries or Orders: Email – products@us.inrevium.com

Part Number : TB-A7-200T-IMG